

In the Abstract of the Disclosure:

Please add the following as the Abstract of the Disclosure after the last page of Claims of and as the last page of the above-identified application:

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--ABSTRACT OF THE DISCLOSURE

The present invention provides a system and method for improving the performance of general purpose processors by expanding at least one source operand to a width greater than the width of either the general purpose register or the data path width. In addition, the present invention provides several classes of instructions which cannot be performed efficiently if the operands are limited to the width and accessible number of general purpose registers. The present invention provides operands which are substantially larger than the data path width of the processor by using a general purpose register to specify a memory address from which at least more than one, but typically several data path widths of data can be read. The present invention also provides for the efficient usage of a multiplier array that is fully used for high precision arithmetic, but is only partly used for other, lower precision operations.

REMARKS**20 Abstract of the Disclosure**

An Abstract of the Disclosure has been added per the May 8, 2001, and May 9, 2001, telephone requests of Examiner M. Monestime. No new matter has been added by the addition of the Abstract of the Disclosure.

25 Conclusion

Attached hereto is a marked-up version of the changes made to the abstract. The attached page is captioned "Version with Markings to Show Changes Made".

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Applicant respectfully requests that a timely Notice of Allowance be issued in this case. In the event that any issue remains unresolved, the Examiner is invited to telephone the undersigned at 650-233-5559.

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Respectfully Submitted,



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Version with Markings to Show Changes Made

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